





Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
B.Tech. in Electrical and Electronics Engineering

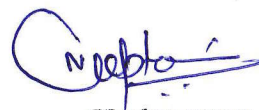
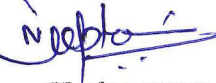
SEMESTER-VIII (2022-2026)

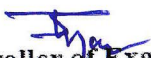
S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	
1	BTEX801	Project / Industrial Internship	0	0	24	12	0	0	0	300	200	500
TOTAL			0	0	24	12	0	0	0	300	200	500


Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;


* Either Student has to undergo Industrial Internship or Project



Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore



Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore


Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore


Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore


Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore